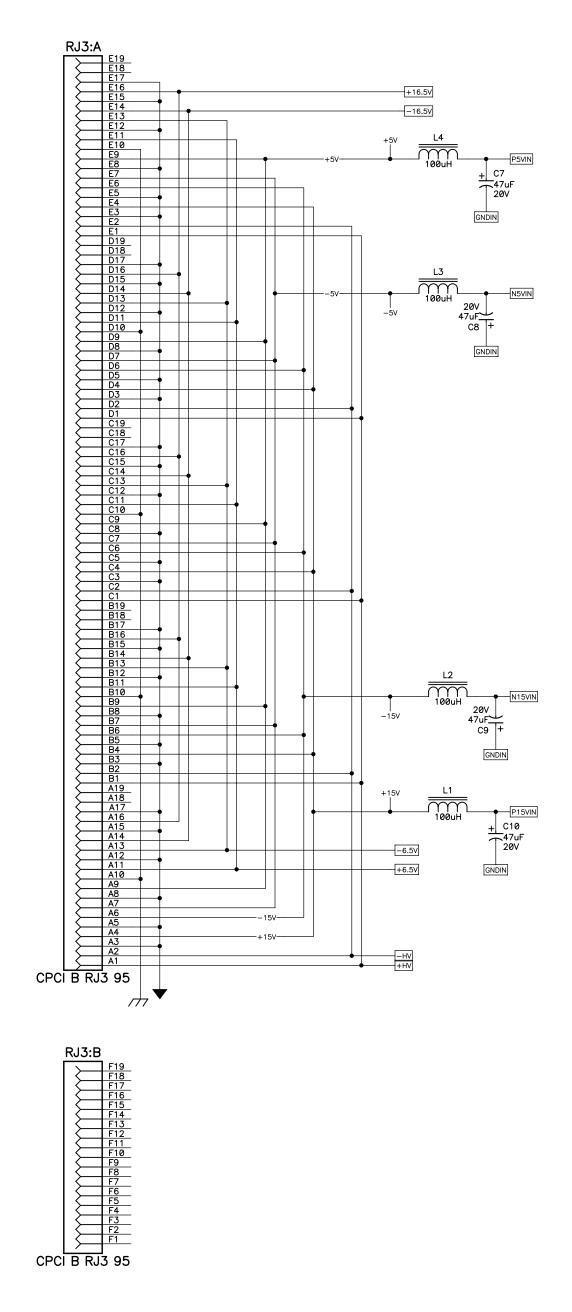


ANALOG POWER

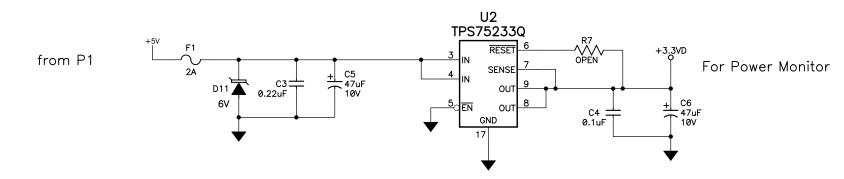
CLK & BIAS BOARD Power Connector



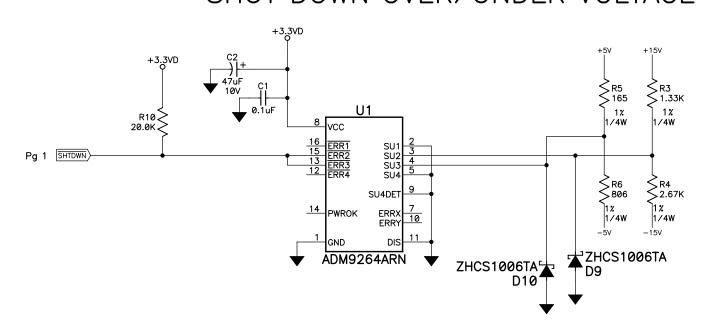
С

D

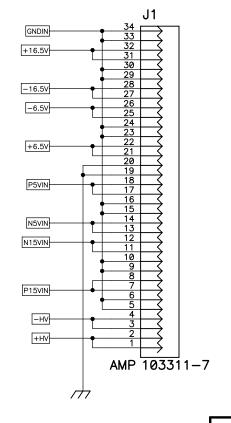
INPUT POWER CONDITIONING

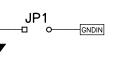


SHUT DOWN OVER/UNDER VOLTAGE



Interface Card Power Input





JP1 should be installed on C/B trans board All clocks & biases referenced to local C/B★

- 1. Install Jumper JP1 only on the board that supplies the entire system.
- See MNSN-AD-01-xxxx and MNSN-AD-01-XXXX for electrical and mechanical specifications of transition boards.

NATIONAL OPTICAL ASTRONOMY OBSERVATORIES SCHEMATIC NEWFIRM Clock & Bias Transition UNIT 4 — A13, A18 NFM-EL-04-2006 D

Modified Date: Wed Aug 10, 2005 RELEASED
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