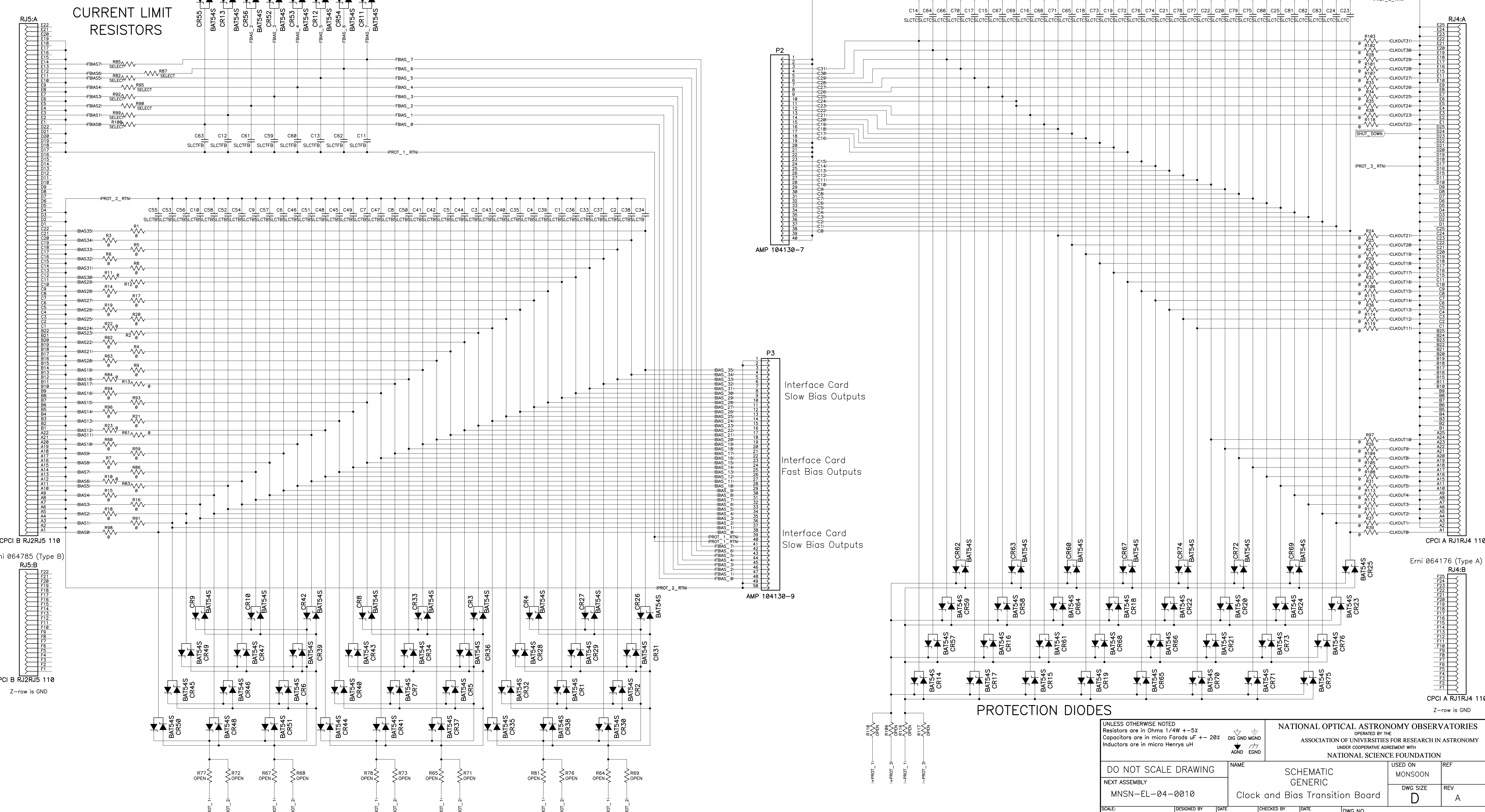


CLK & BIAS BOARD
Bias and FastBias Outputs

Interface Card
Clock Outputs

CLK & BIAS BOARD
Clock Outputs



UNLESS OTHERWISE NOTED
Resistors are in Ohms 1/4W +-5%
Capacitors are in micro Farads uF +- 20%
Inductors are in micro Henrys uH

NATIONAL OPTICAL ASTRONOMY OBSERVATORIES
OPERATED BY THE
ASSOCIATION OF UNIVERSITIES FOR RESEARCH IN ASTRONOMY
UNDER COOPERATIVE AGREEMENT WITH
NATIONAL SCIENCE FOUNDATION

| | | | |
|----------------------|----------------------|----------|-----|
| DO NOT SCALE DRAWING | NAME | USED ON | REF |
| NEXT ASSEMBLY | SCHEMATIC GENERIC | MONSOON | |
| MNSN-EL-04-0010 | | DWG SIZE | REV |
| | | D | A |

| | | | | | |
|---------------------------------|----------------------------|------------------------|-------------|------|----------|
| SCALE: | DESIGNED BY Peter Moore | DATE 20th Sept 2004 | CHECKED BY | DATE | DWG NO |
| DWG PRODUCED USING PCAD 2004 | DRAWN BY Peter Moore | DATE | APPROVED BY | DATE | |
| | PRINT ISSUED | DATE | APPROVED BY | DATE | RELEASED |

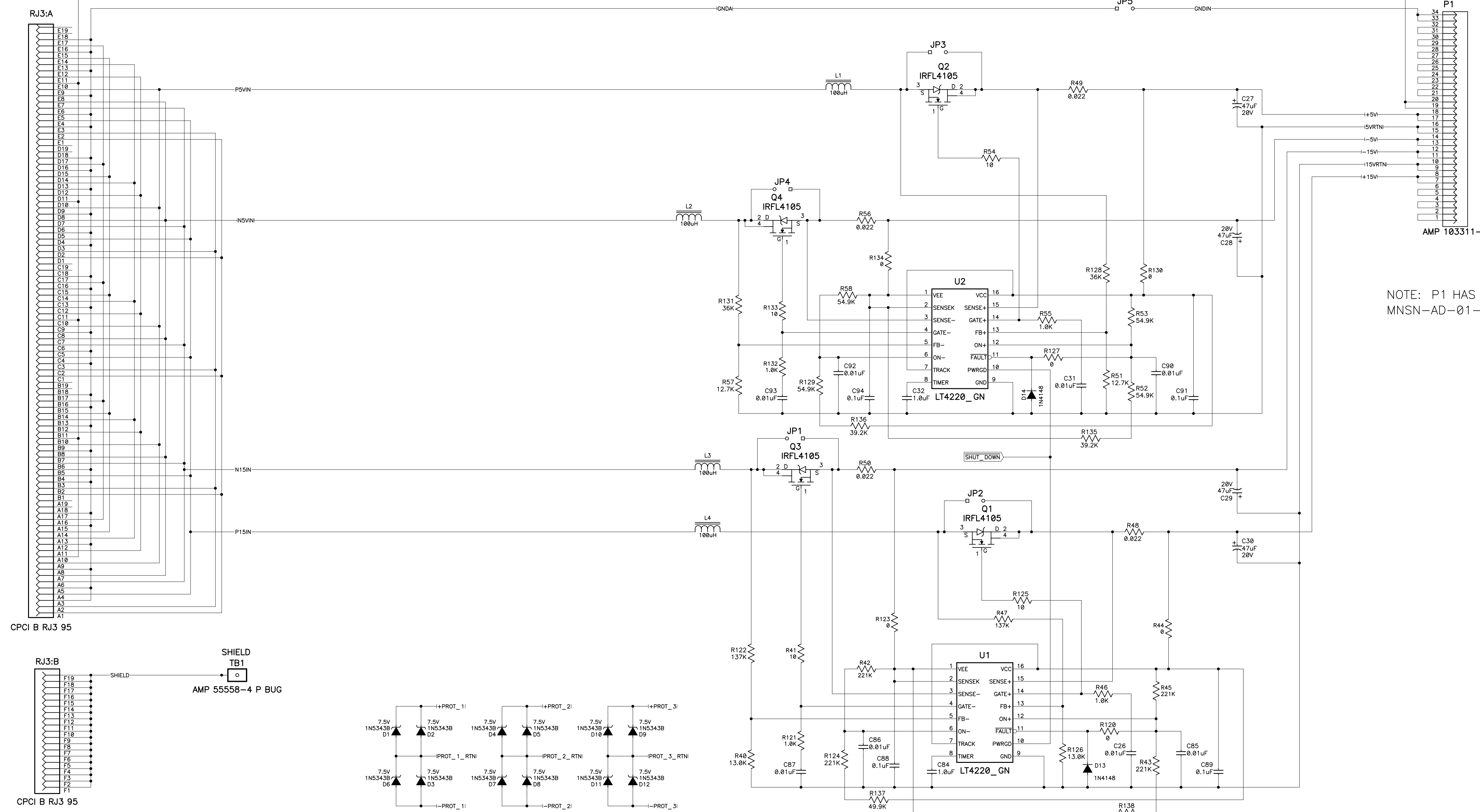
Modified Date: Thu Oct 06, 2005
Print Date: Mon Oct 10, 2005

MNSN-EL-04-2010
Sheet 1 of 2

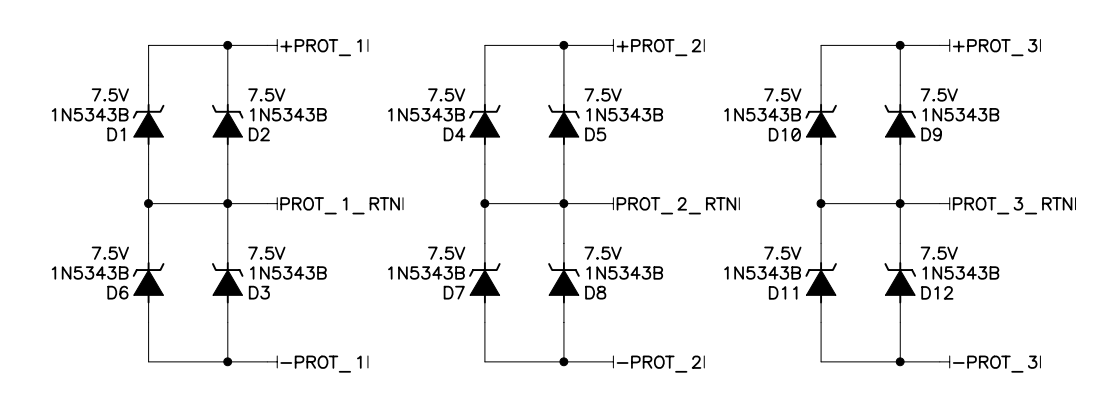
| REVISIONS | | | | | |
|-----------|---|-----------|---------|------|---------|
| LTR | DESCRIPTION | ECR | DATE | BY | APPVD |
| A | reposition inductors in circuit, change to shield ground connection, add resistors, value changes, cap land pattern chd | MNSN-0110 | 05oct05 | drms | p.moore |

CLK & BIAS BOARD
Power Connector

Interface Card
Power Input



NOTE: P1 HAS A POSITIONAL RESTRICTION SEE DRAWING MNSN-AD-01-0010 FOR PCB CARD DIMENSIONS



PROTECTION CLAMPS

Supplies are +/- 5% over / under protected.
Current limit set for 2.1 amps during normal operation and 680 ma during start up cycle.

| NATIONAL OPTICAL ASTRONOMY OBSERVATORIES | | | | | |
|--|-----------|-----|-----|--|--|
| NAME | SCHEMATIC | | | | |
| Clock and Bias Transition Board | | | | | |
| DWG NO | SIZE | REF | REV | | |
| MNSN-EL-04-2010 | D | | A | | |
| RELEASED | | | | | |