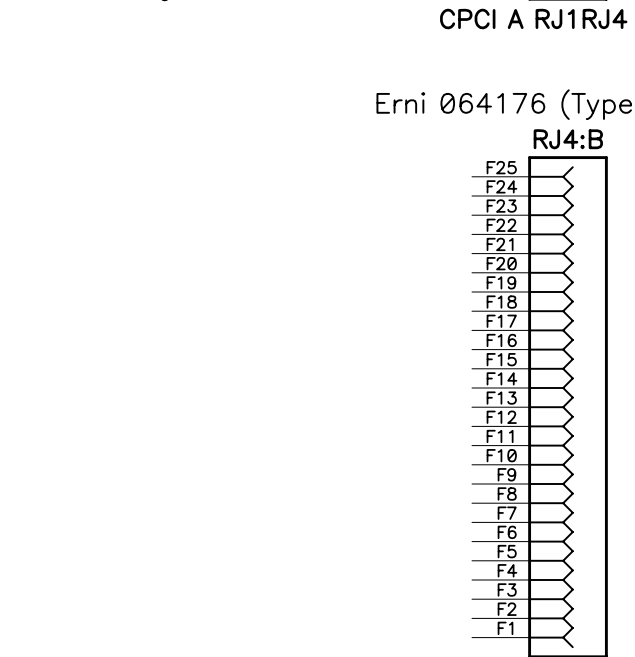
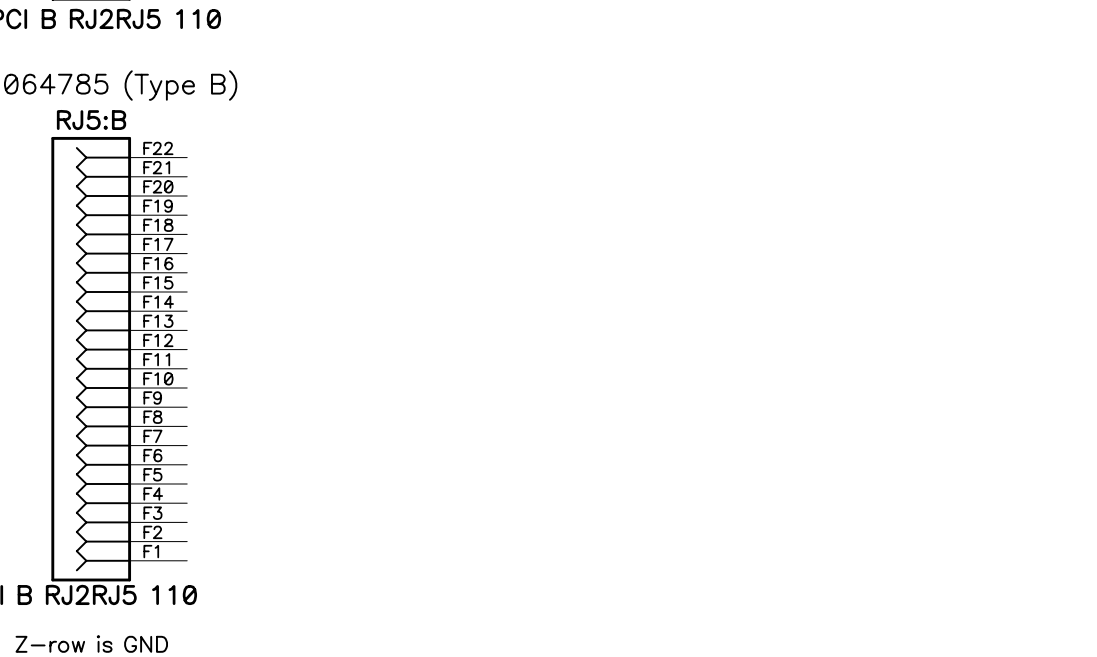
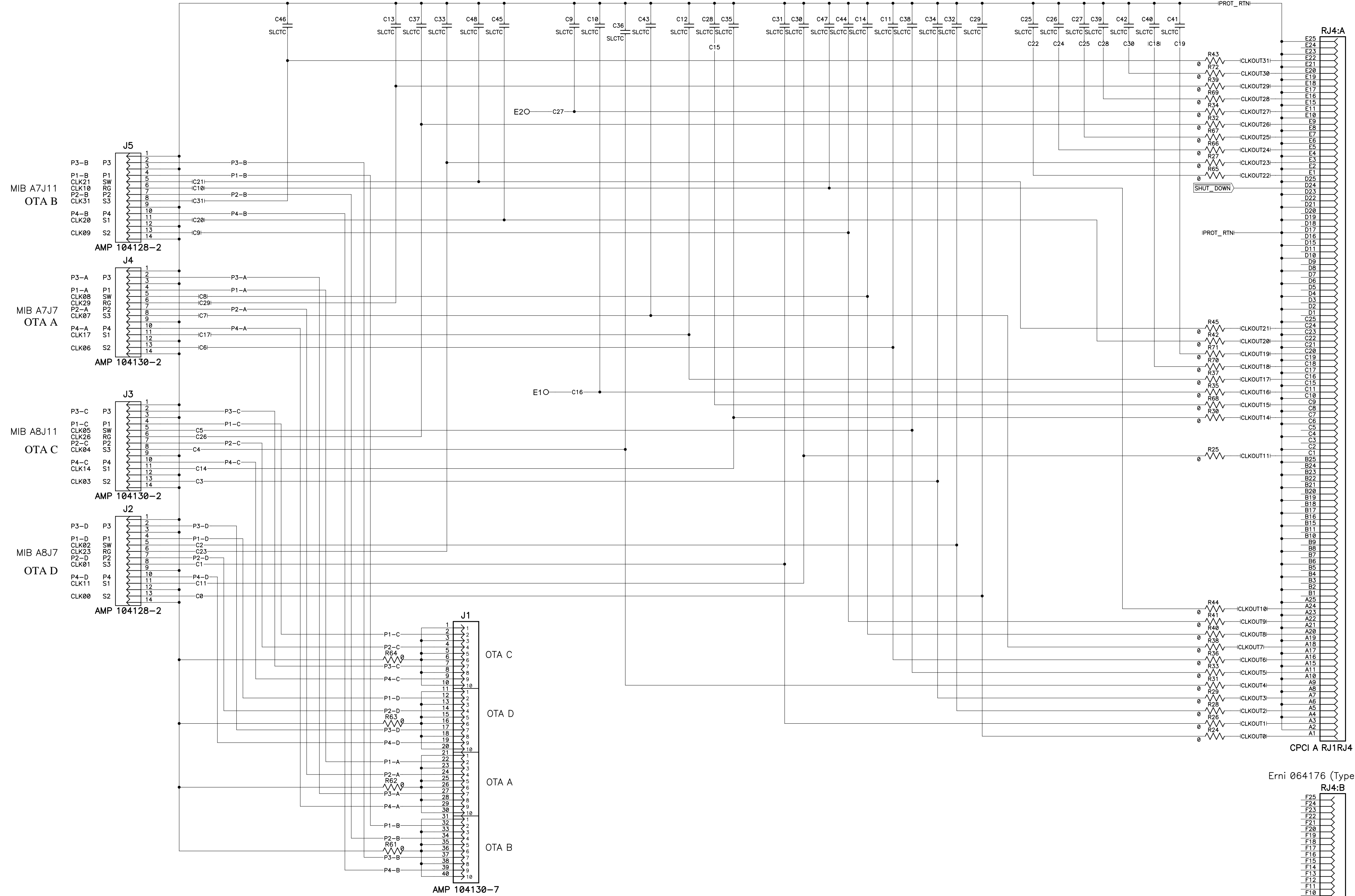
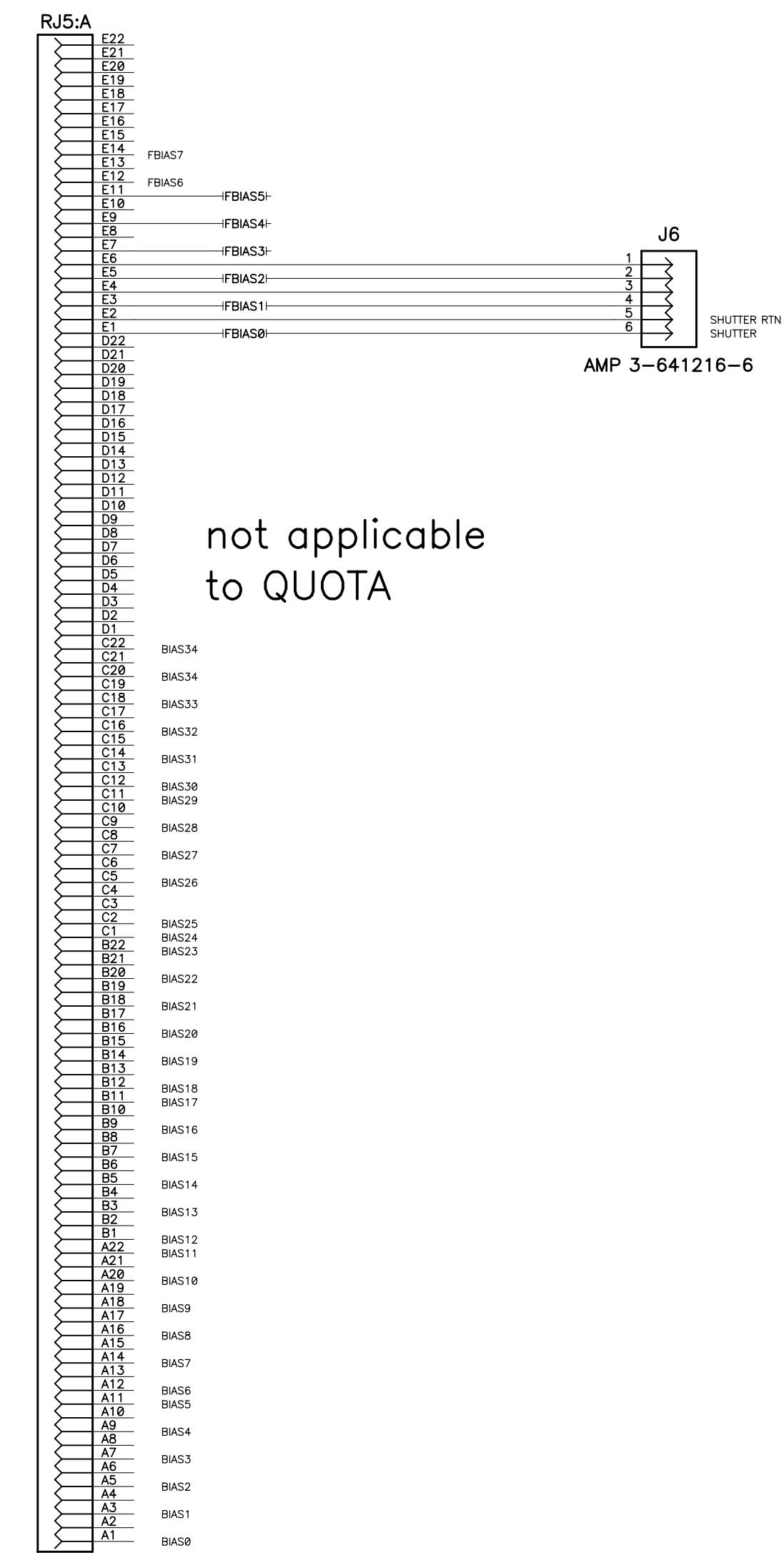


CLK & BIAS BOARD
Bias and FastBias Outputs

Interface Card
Clock Outputs

CLK & BIAS BOARD
Clock Outputs



UNLESS OTHERWISE NOTED
Resistors are in Ohms 1/4W +-5%
Capacitors are in micro Farads uF +-20%
Inductors are in micro Henrys uH

NATIONAL OPTICAL ASTRONOMY OBSERVATORIES
OPERATED BY THE
ASSOCIATION OF UNIVERSITIES FOR RESEARCH IN ASTRONOMY
UNDER COOPERATIVE AGREEMENT WITH
NATIONAL SCIENCE FOUNDATION

DO NOT SCALE DRAWING	NAME	USED ON MONSOON QUOTA	REF
NEXT ASSEMBLY	SCHEMATIC QUOTA Clock and Bias Transition Board	DWG SIZE	REV
MNSN-EL-04-0016		D	OD
SCALE:	DESIGNED BY Peter Moore	DATE 26JUN06	CHECKED BY DATE
DWG PRODUCED USING PCAD 2004	DRAWN BY Peter Moore	DATE	APPROVED BY DATE
	PRINT ISSUED	DATE	APPROVED BY DATE
		RELEASED	

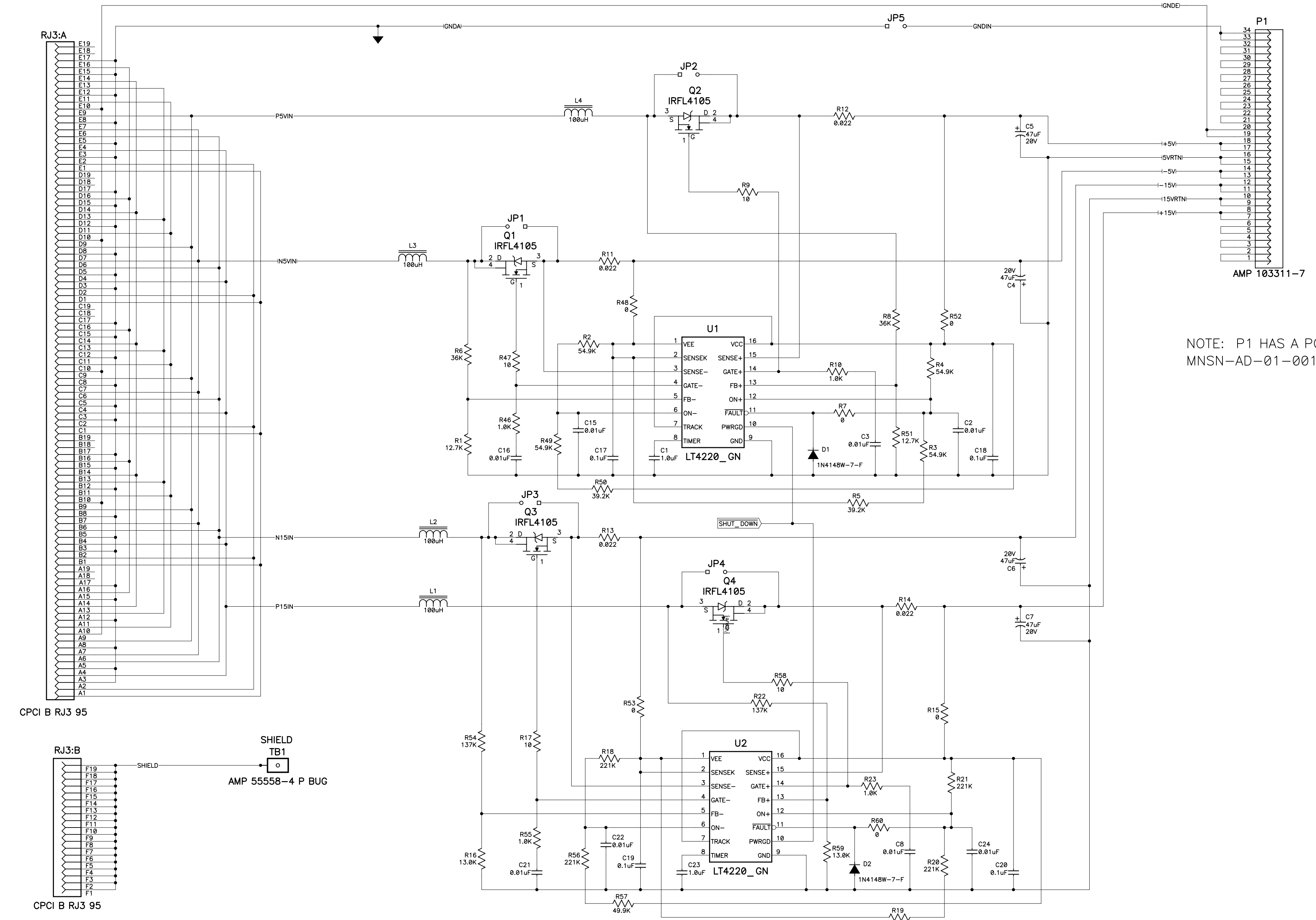
Modified Date: Fri Dec 08, 2006
Print Date: Fri Dec 08, 2006

MNSN-EL-04-2016
Sheet 1 of 2

REVISIONS					
LTR	DESCRIPTION	ECR	DATE	BY	APPVD
OD	INITIAL RELEASE		01DEC06	DMS	P.MOORE

CLK & BIAS BOARD
Power Connector

Interface Card
Power Input



NOTE: P1 HAS A POSITIONAL RESTRICTION SEE DRAWING
MNSN-AD-01-0010 FOR PCB CARD DIMENSIONS

Supplies are +/- 5% over / under protected.
Current limit set for 2.1 amps during normal operation
and 680 ma during start up cycle.

NATIONAL OPTICAL ASTRONOMY OBSERVATORIES					
NAME SCHEMATIC					
QUOTA					
Clock and Bias Transition Board					
DWG NO	SIZE	REF	REV		
MNSN-EL-04-2016	D		OD		
RELEASED					
Modified Date: Fri Dec 08, 2006 Print Date: Fri Dec 08, 2006					
Sheet 2 of 2					