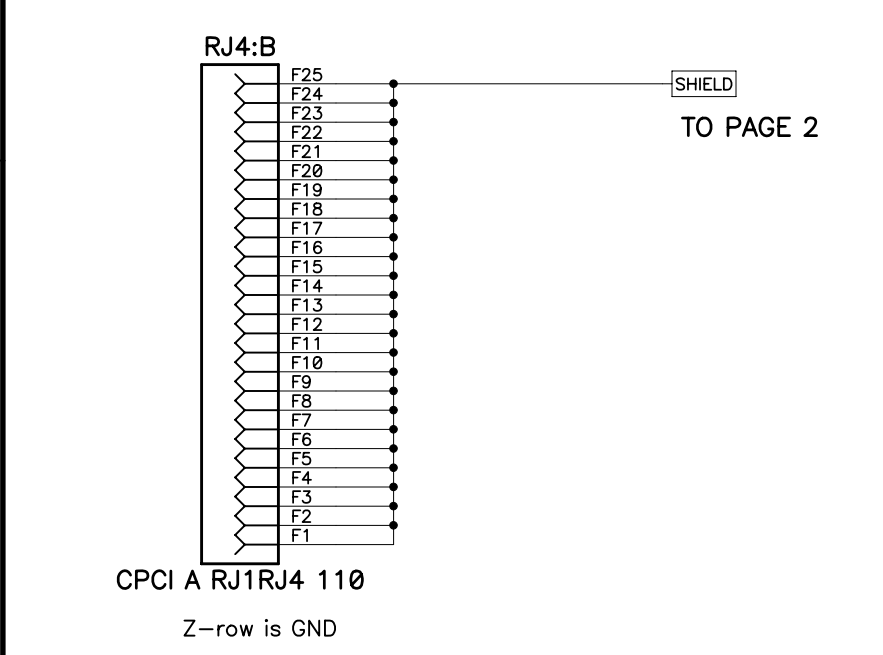
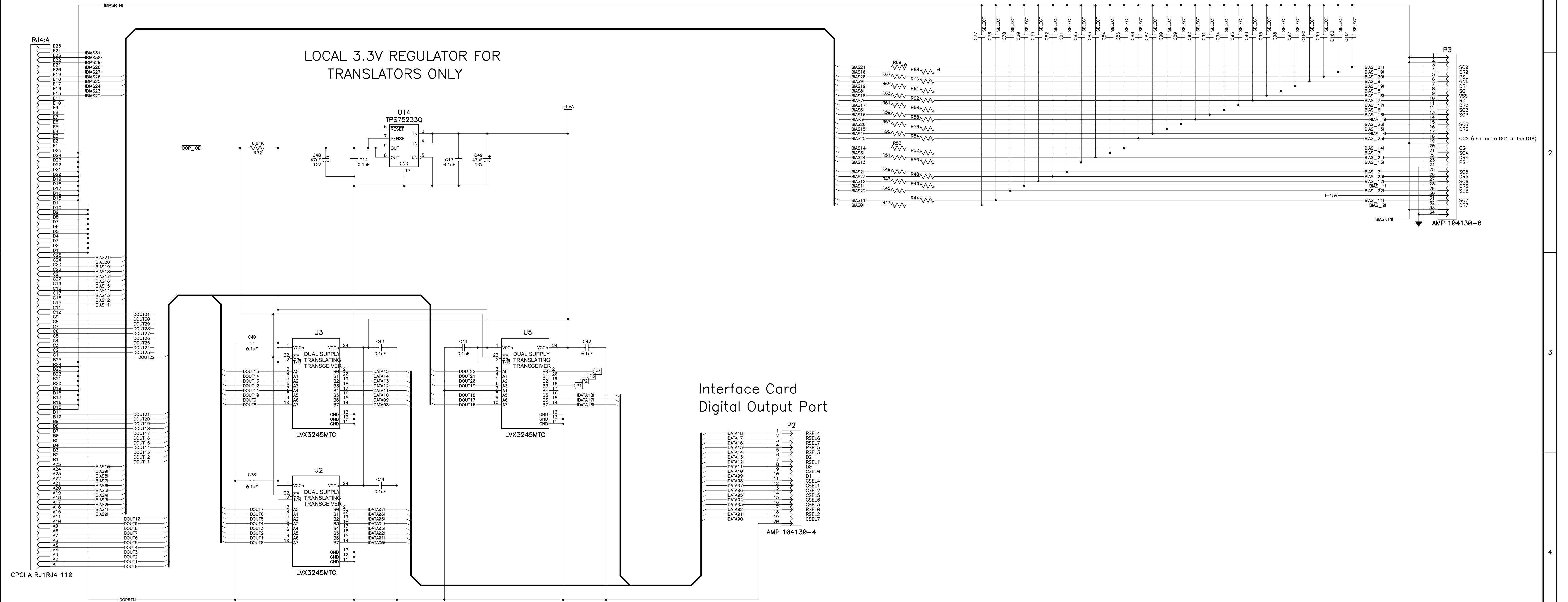


CCD ACQUISITION BOARD
High Voltage Bias and
Digital Output Port Outputs



UNLESS OTHERWISE NOTED
Resistors are in Ohms 1/4W +-5%
Capacitors are in micro Farads uF +- 20%
Inductors are in micro Henrys uH

NATIONAL OPTICAL ASTRONOMY OBSERVATORIES
OPERATED BY THE
ASSOCIATION OF UNIVERSITIES FOR RESEARCH IN ASTRONOMY
UNDER COOPERATIVE AGREEMENT WITH
NATIONAL SCIENCE FOUNDATION

DO NOT SCALE DRAWING	NAME	USED ON	REF
NEXT ASSEMBLY	SCHEMATIC	MONSOON	
MNSN-EL-04-0015	QUOTA	DWG SIZE	REV
	CCD Acq Transition Board	D	OD

SCALE:	DESIGNED BY	DATE	CHECKED BY	DATE	DWG NO
DWG PRODUCED USING	Peter Moore	16JUN06			
PCAD 2004	DRAWN BY	DATE	APPROVED BY	DATE	
	Peter Moore				
	PRINT ISSUED	DATE	APPROVED BY	DATE	RELEASED

MNSN-EL-04-2015

Modified Date: Thu Feb 01, 2007
Print Date: Thu Feb 01, 2007

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