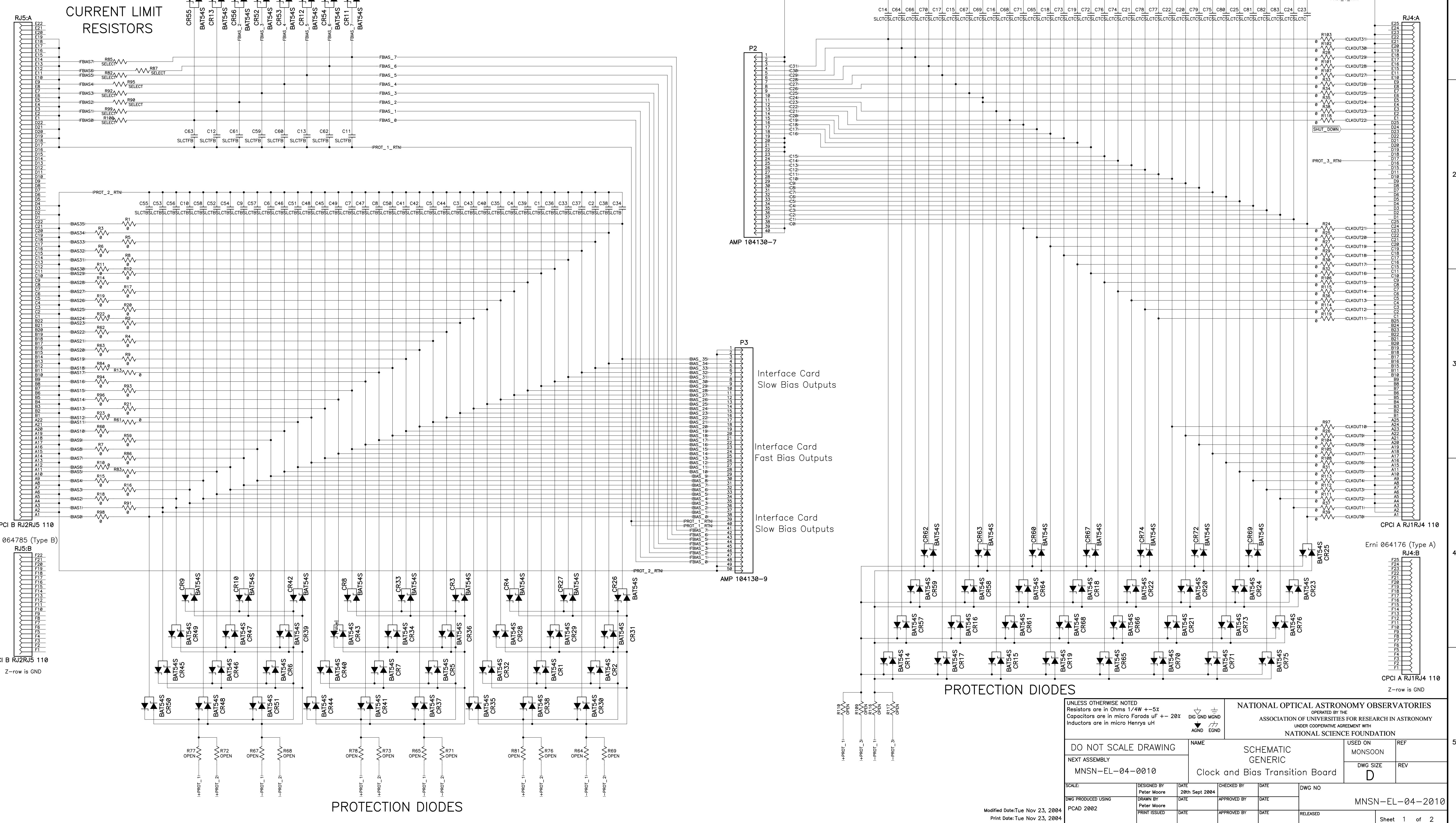


REVISIONS				
LTR	DESCRIPTION	ECR	DATE	APPVD

CLK & BIAS BOARD
Bias and FastBias Outputs

Interface Card
Clock Outputs

CLK & BIAS BOARD
Clock Outputs



UNLESS OTHERWISE NOTED
Resistors are in Ohms 1/4W +/- 5%
Capacitors are in micro Farads uF +/- 20%
Inductors are in micro Henrys uH

NATIONAL OPTICAL ASTRONOMY OBSERVATORIES
OPERATED BY THE
ASSOCIATION OF UNIVERSITIES FOR RESEARCH IN ASTRONOMY
UNDER COOPERATIVE AGREEMENT WITH
NATIONAL SCIENCE FOUNDATION

DO NOT SCALE DRAWING	NAME	USED ON	REF
NEXT ASSEMBLY	SCHMATIC	MONSOON	
MNSN-EL-04-0010	Generic	DWG SIZE	REV
	Clock and Bias Transition Board	D	

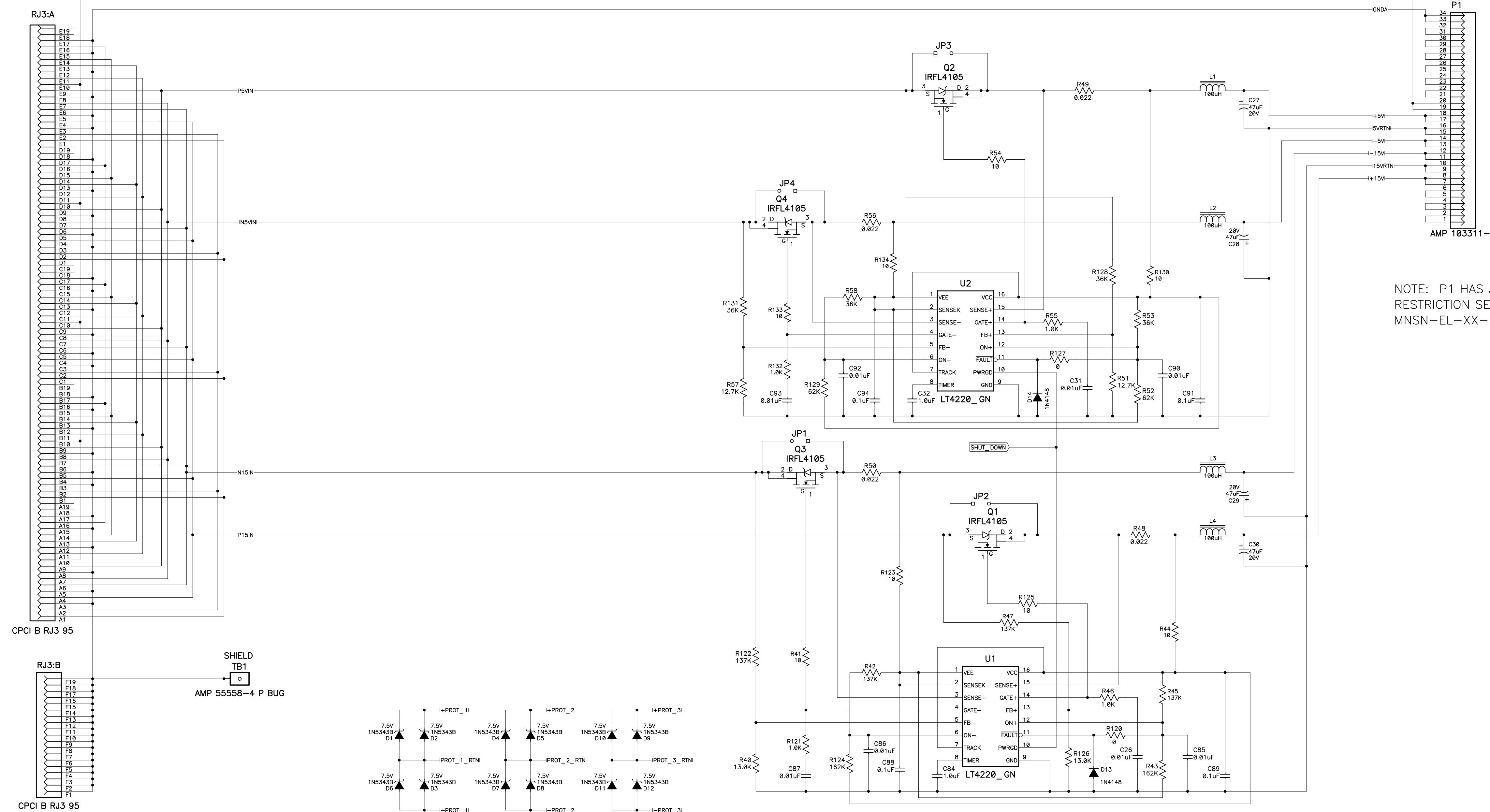
SCALE:	DESIGNED BY	DATE	CHECKED BY	DATE	DWG NO
	Peter Moore	29th Sept 2004			
DWG PRODUCED USING	DRAWN BY	DATE	APPROVED BY	DATE	
PCAD 2002	Peter Moore				
	PRINT ISSUED	DATE	APPROVED BY	DATE	RELEASED

Modified Date: Tue Nov 23, 2004
Print Date: Tue Nov 23, 2004

MNSN-EL-04-2010
Sheet 1 of 2

CLK & BIAS BOARD
Power Connector

Interface Card
Power Input



NOTE: P1 HAS A POSITIONAL
RESTRICTION SEE DRAWING
MNSN-EL-XX-XXXX FOR DIMENSIONS

PROTECTION CLAMPS

Supplies are +/- 5% over / under protected.
Current limit set for 2.1 amps during normal operation
and 680 ma during start up cycle.

NATIONAL OPTICAL ASTRONOMY OBSERVATORIES			
NAME	SCHEMATIC GENERIC Clock and Bias Transition Board		
DWG NO	SIZE	REF	REV
MNSN-EL-04-2010	D		
RELEASED	Sheet 2 of 2		

Modified Date: Tue Nov 23, 2004
Print Date: Tue Nov 23, 2004