

Channel 18

Channel 19

Channel 20

Channel 21

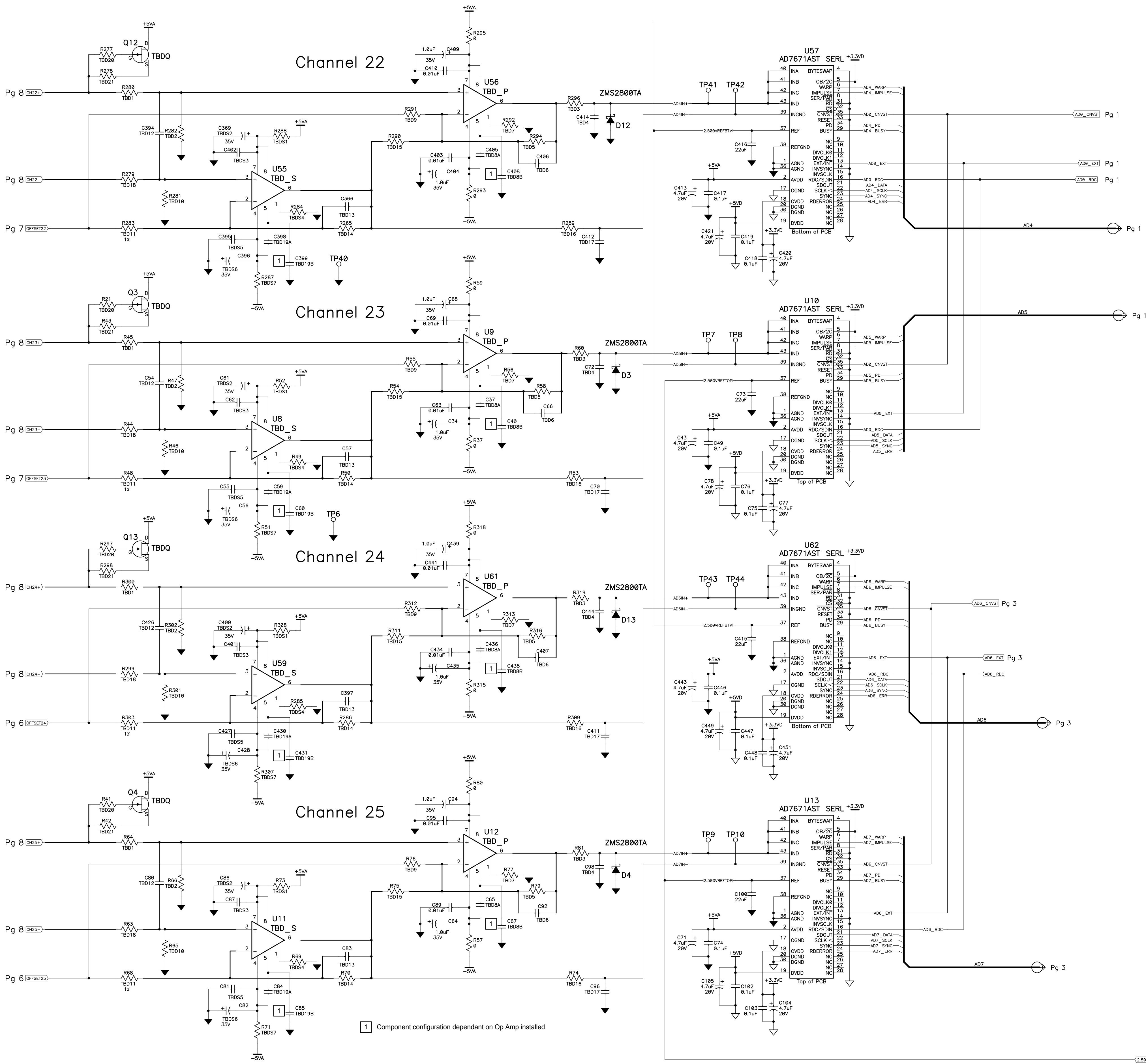
ADC CPLD  
Coolrunner XPLA

U7  
XCR3384XLPQ208  
XCR3384XL-7-PQ208C

TEMPERATURE  
SENSORS

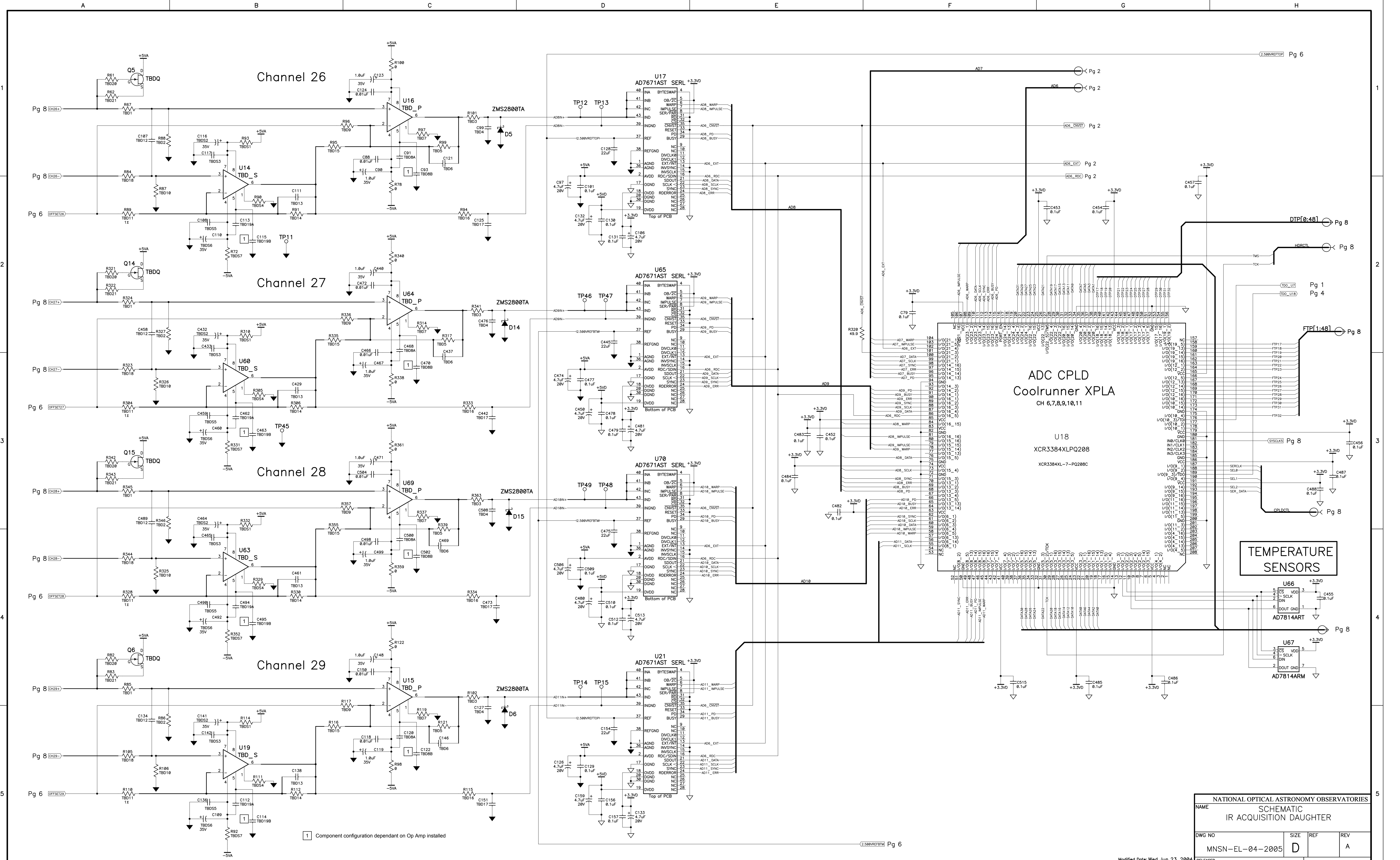
1 Component configuration dependant on Op Amp installed

UNLESS OTHERWISE NOTED Resistors are in ohms 1/W + -5% Capacitors are in micro farads uF + -20% Inductors are in micro Henrys uH		NATIONAL OPTICAL ASTRONOMY OBSERVATORIES OPERATED BY THE ASSOCIATION OF UNIVERSITIES FOR RESEARCH IN ASTRONOMY UNDER COOPERATIVE AGREEMENT WITH NATIONAL SCIENCE FOUNDATION	
DO NOT SCALE DRAWING	NAME SCHEMATIC	USED ON MONSOON	REF
NEXT ASSEMBLY MNSN-EL-10-0200	IR ACQUISITION DAUGHTER	DWG SIZE D	REV A
SCALE:	DESIGNED BY STARR/GP	DATE 21 Oct 02	CHECKED BY DATE
DWG PRODUCED USING PCAD 2001	DRAWN BY Stover	DATE 21OCT02	APPROVED BY DATE
PRINT ISSUED	DATE	APPROVED BY	DATE
DWG NO FORMERLY MON.3000.0015		MNSN-EL-04-2005	
MNSN-EL-04-2005		Modified Date: Wed Jun 23, 2004	
Sheet 1 of 8		Pg 6	



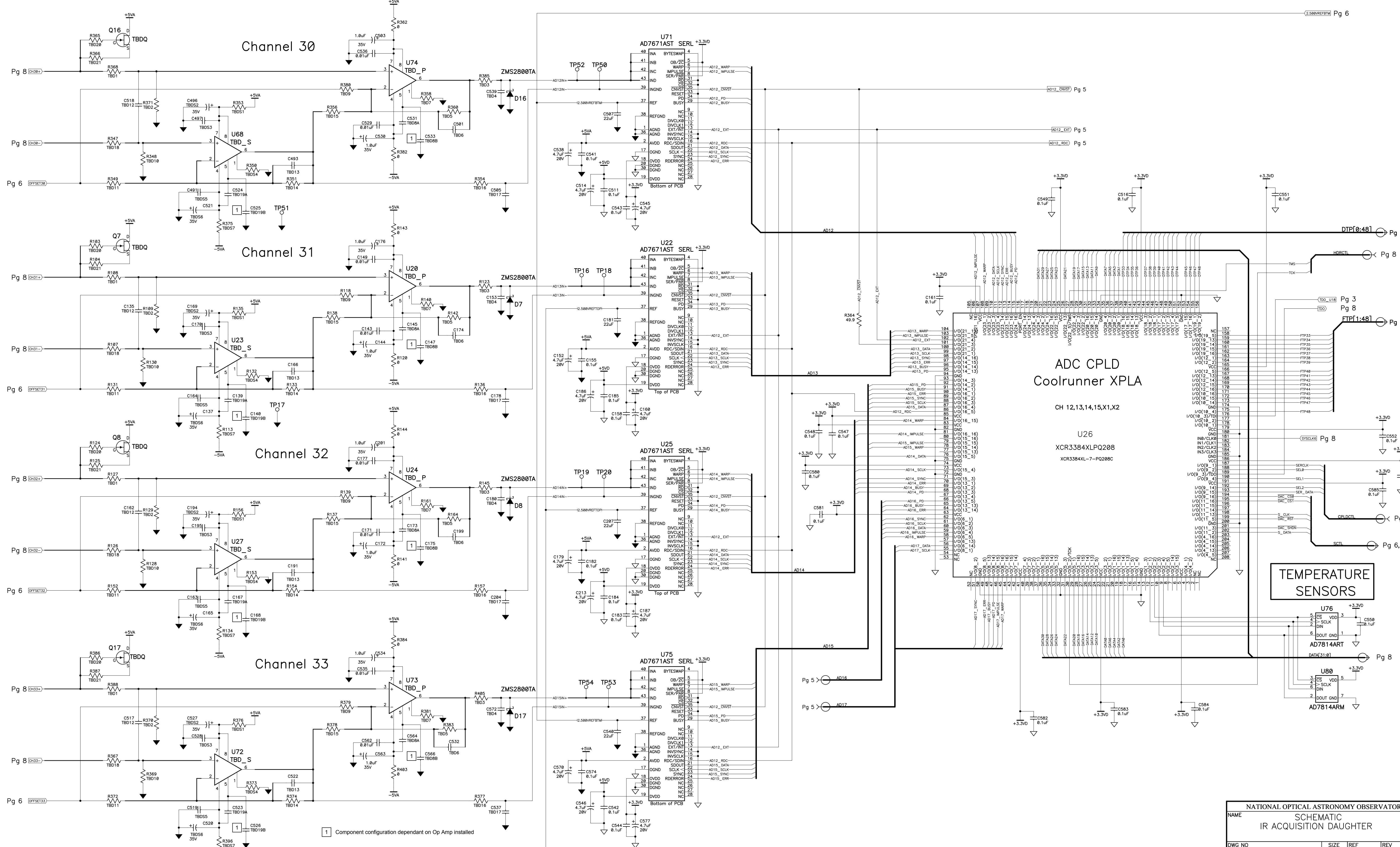
1 Component configuration dependent on Op Amp installed

NATIONAL OPTICAL ASTRONOMY OBSERVATORIES				
NAME SCHEMATIC IR ACQUISITION DAUGHTER				
DWG NO	SIZE	REF	REV	
MNSN-EL-04-2005	D		A	
RELEASED				
Sheet 2 of 8				



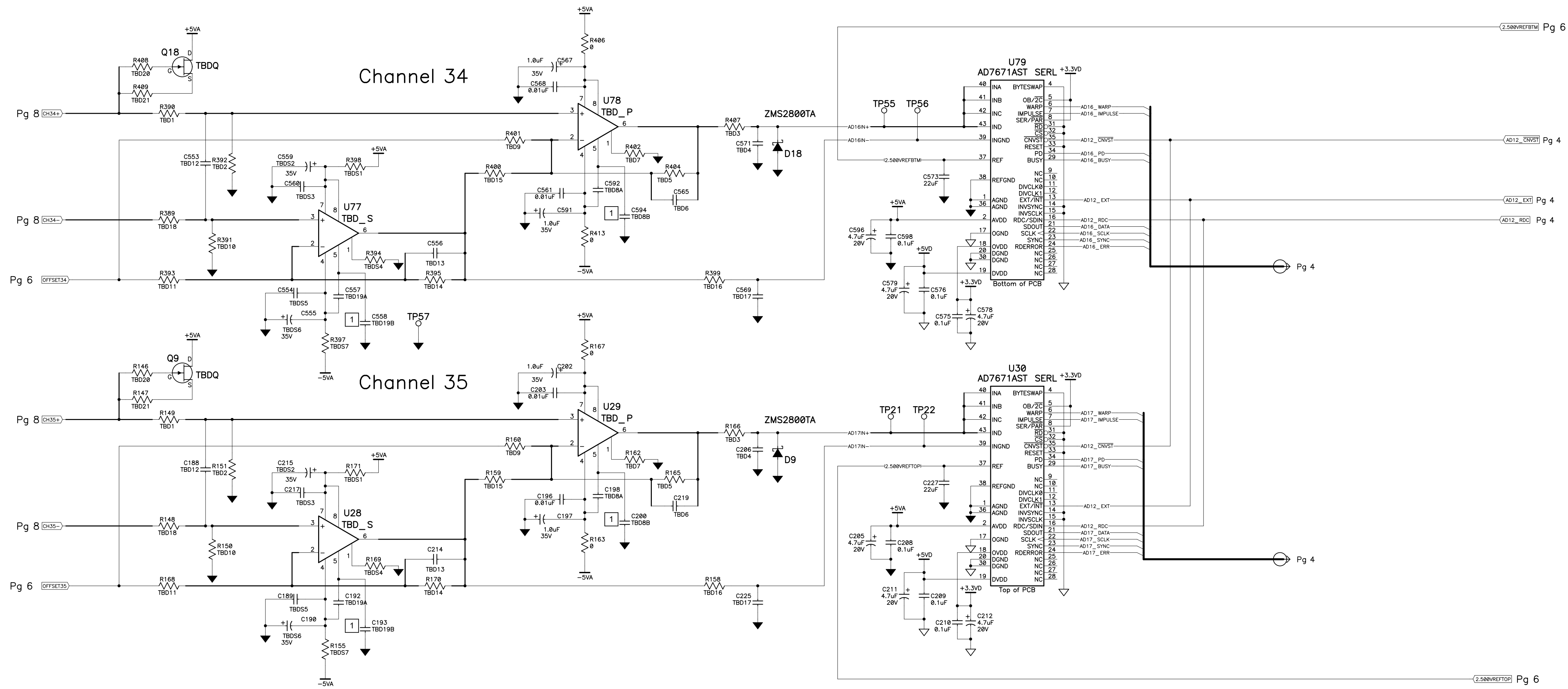
1 Component configuration dependant on Op Amp installed

NATIONAL OPTICAL ASTRONOMY OBSERVATORIES			
NAME SCHEMATIC IR ACQUISITION DAUGHTER			
DWG NO	SIZE	REF	REV
MNSN-EL-04-2005	D		A
RELEASED			
Modified Date: Wed Jun 23, 2004 MNSN-EL-04-2005_rASRH Date: Wed Jun 23, 2004			
Sheet 3		of 8	



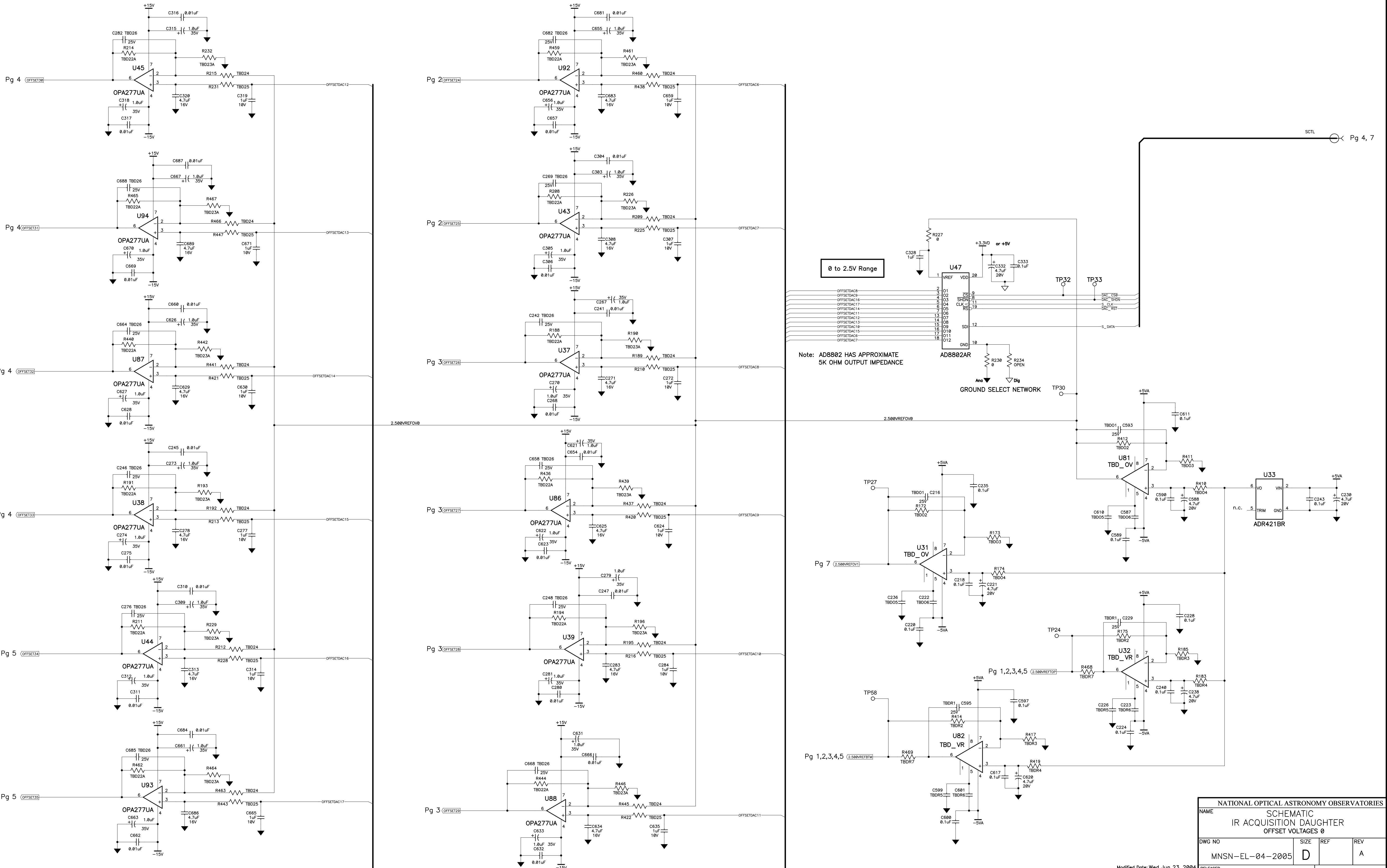
1 Component configuration dependent on Op Amp installed

NATIONAL OPTICAL ASTRONOMY OBSERVATORIES				
NAME	SCHEMATIC IR ACQUISITION DAUGHTER			
DWG NO	SIZE	REF	REV	
MNSN-EL-04-2005	D		A	
RELEASED	MNSN-EL-04-2005	Modified Date: Wed Jun 23, 2004	Sheet 4 of 8	



1 Component configuration dependent on Op Amp installed

NATIONAL OPTICAL ASTRONOMY OBSERVATORIES			
NAME SCHEMATIC IR ACQUISITION DAUGHTER			
DWG NO	SIZE	REF	REV
MNSN-EL-04-2005	D		A
RELEASED			
Sheet 5 of 8			



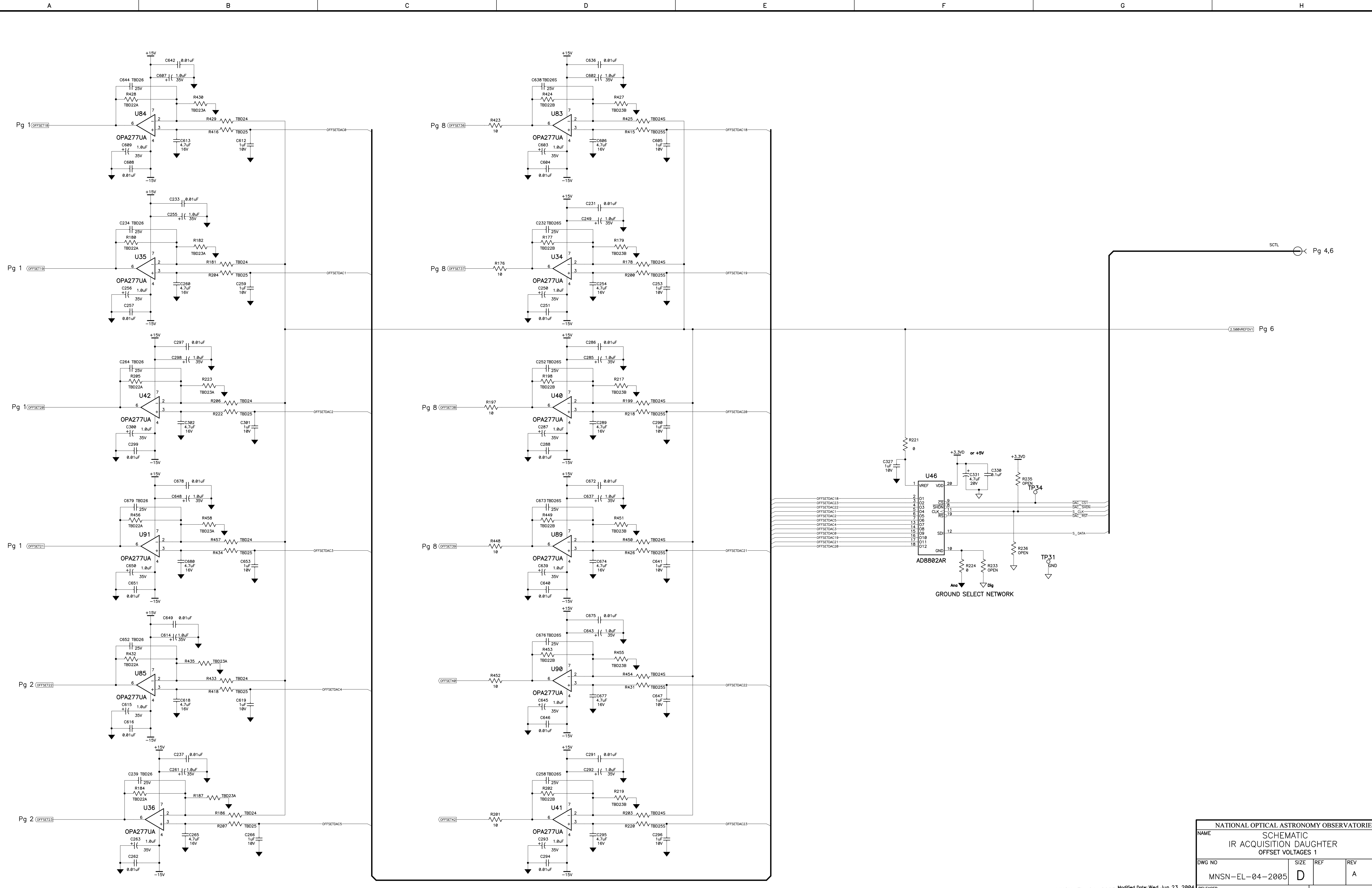
SCTL Pg 4, 7

0 to 2.5V Range

Note: AD8802AR HAS APPROXIMATE 5K OHM OUTPUT IMPEDANCE

GROUND SELECT NETWORK

NATIONAL OPTICAL ASTRONOMY OBSERVATORIES				
NAME SCHEMATIC				
IR ACQUISITION DAUGHTER				
OFFSET VOLTAGES 0				
DWG NO	SIZE	REF	REV	
MNSN-EL-04-2005	D		A	
RELEASED				
Modified Date: Wed Jun 23, 2004				
Print Date: Wed Jun 23, 2004				
MNSN-EL-04-2005_P4.SCH			Sheet 6 of 8	



NATIONAL OPTICAL ASTRONOMY OBSERVATORIES				
NAME	SCHEMATIC IR ACQUISITION DAUGHTER OFFSET VOLTAGES 1			
DWG NO	MNSN-EL-04-2005	SIZE	REF	REV
RELEASED		D		A
Sheet 7 of 8				

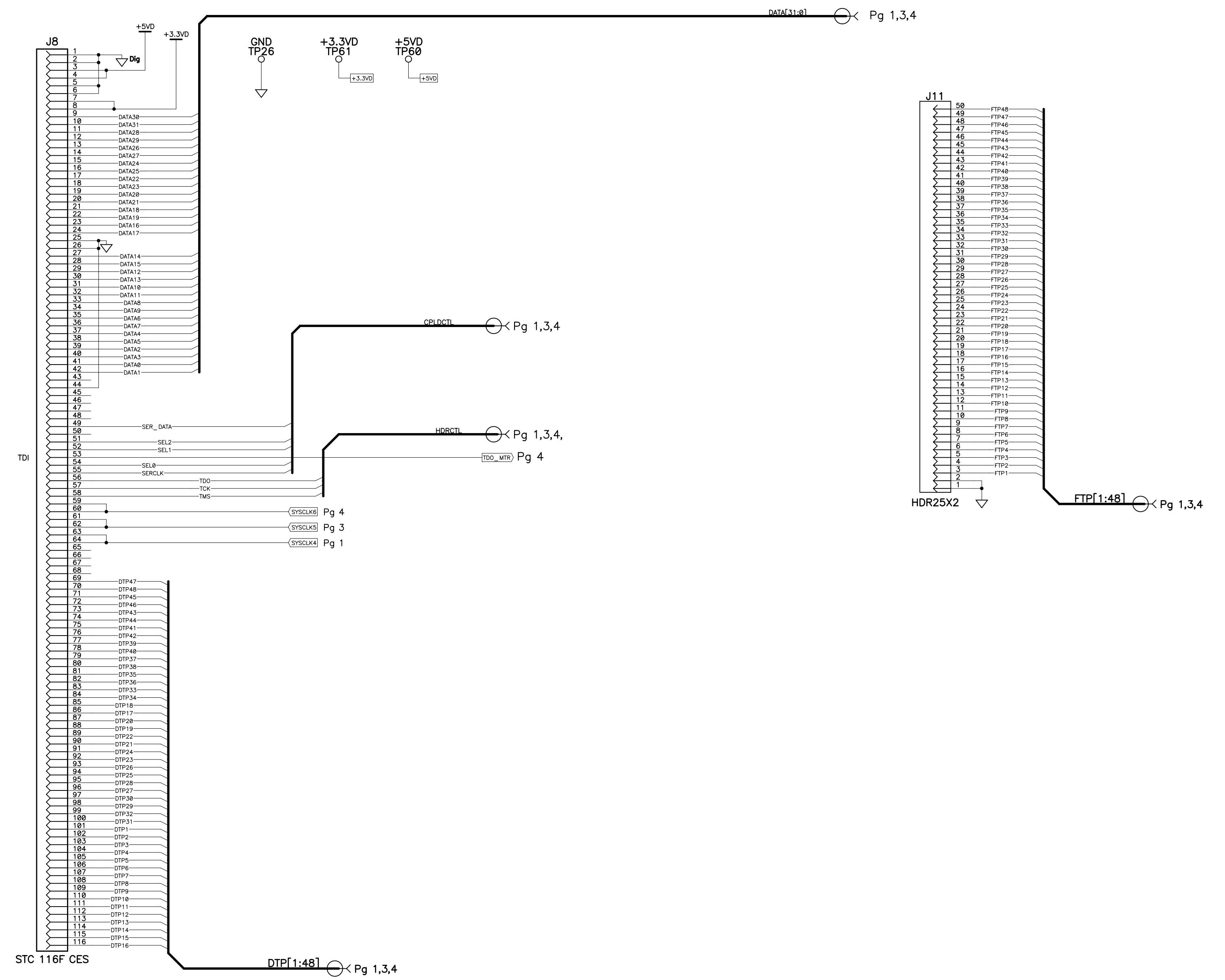
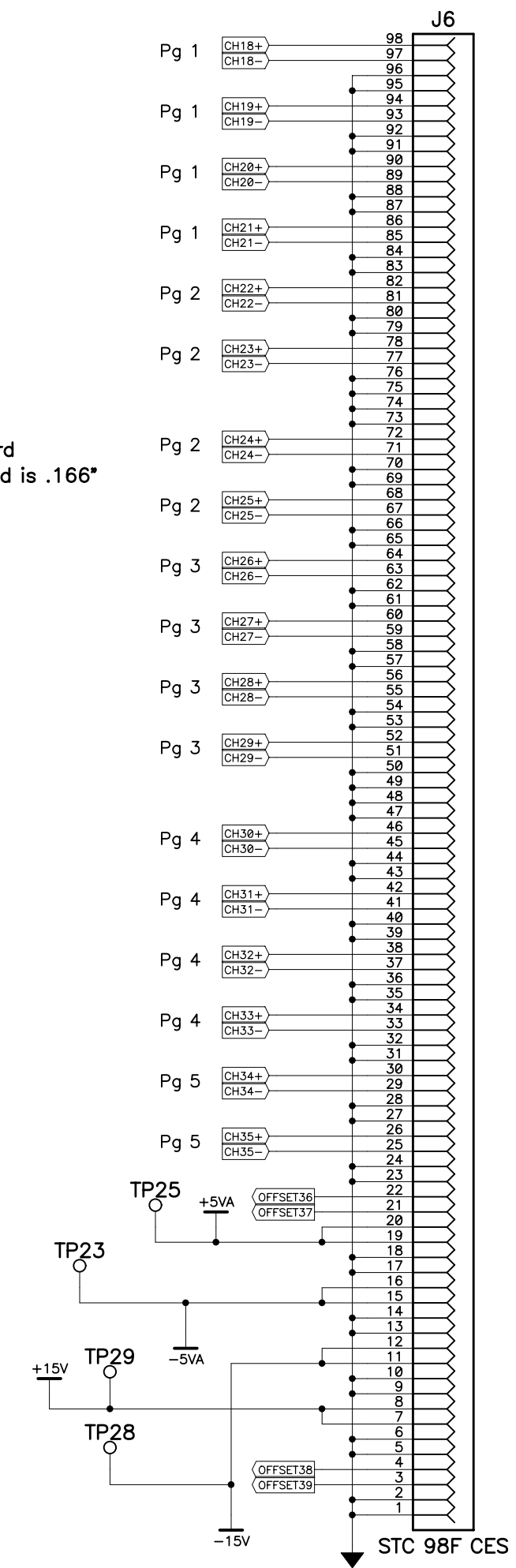
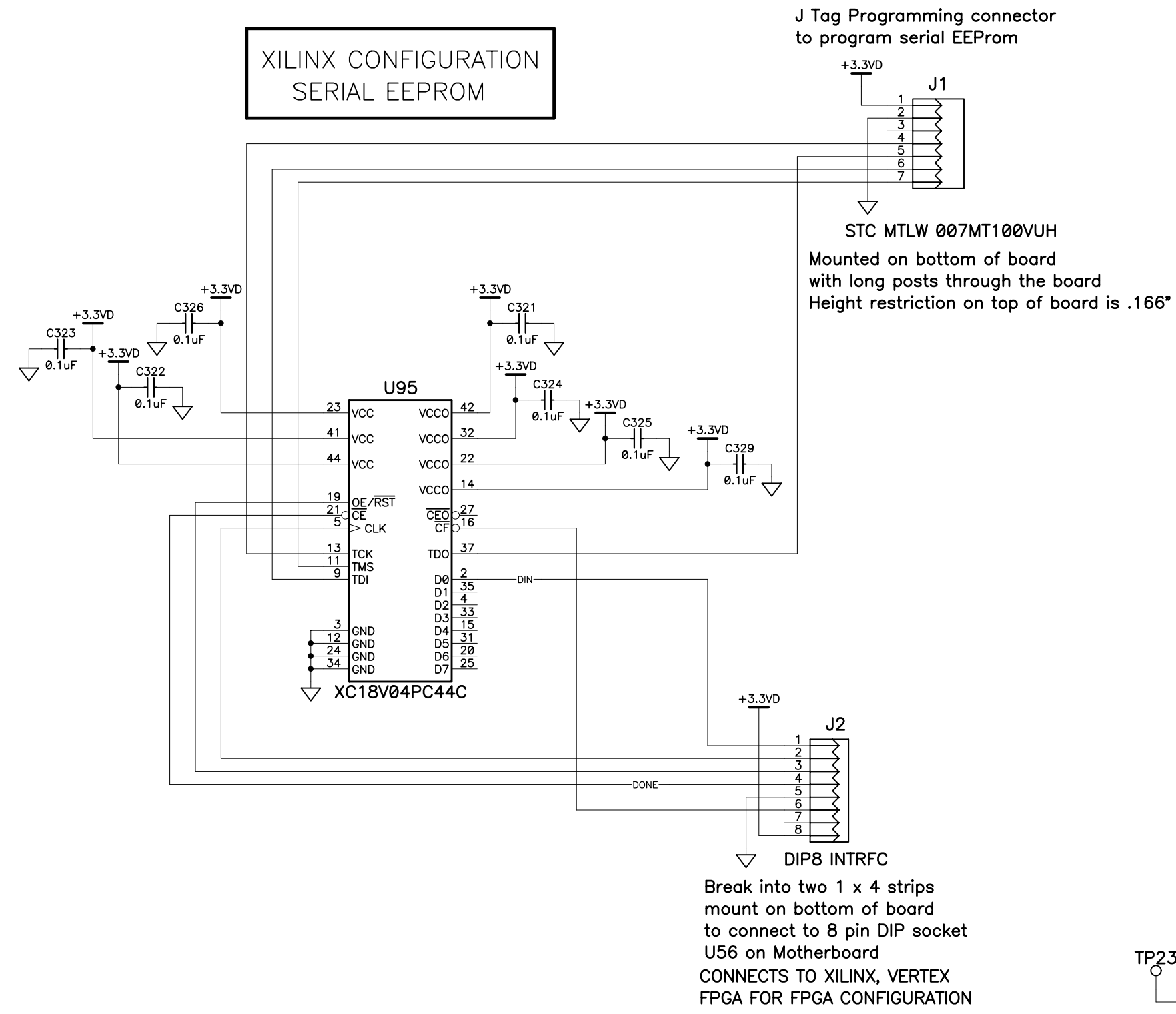
REVISIONS					
LTR	DESCRIPTION	ECR	DATE	BY	APPVD
A					

NOTE: The JTAG connector (J1) is used to connect to JTAG programming connector, for programming the serial EEPROM U95. U95 then programs the Xilinx XCV300E Virtex FPGA on the IR Acquisition Mother board through J2.

### mezzanine header to Mother Board

### mezzanine connector from Mother Board

Note: The square pad markings on J8 of the mother board are incorrect. The gender was swapped and J8 and J10 are now combined into one 116 pin connector. The pin out on this schematic is showing the correct gender and pin numbering. See Mother board schematic for graphic representation.



NATIONAL OPTICAL ASTRONOMY OBSERVATORIES					
NAME					
SCHEMATIC					
IR ACQUISITION DAUGHTER					
Mother board interface					
DWG NO	SIZE	REF	REV		
MNSN-EL-04-2005	D		A		
RELEASED					
Sheet 8 of 8					



1
2
3
4
5
ES