

## Bias Enable problem at Power on - Solution

Add three 8.66k value 0805 size resistors to the locations shown below.

Resistors are pull downs to ground on from Pin 1 of U88, U89 and U96, which are the Bias Enable 0,1, 2 signals.

Remove resistor R315 from the top side of the board, R315 is located near the corner of the FPGA.

