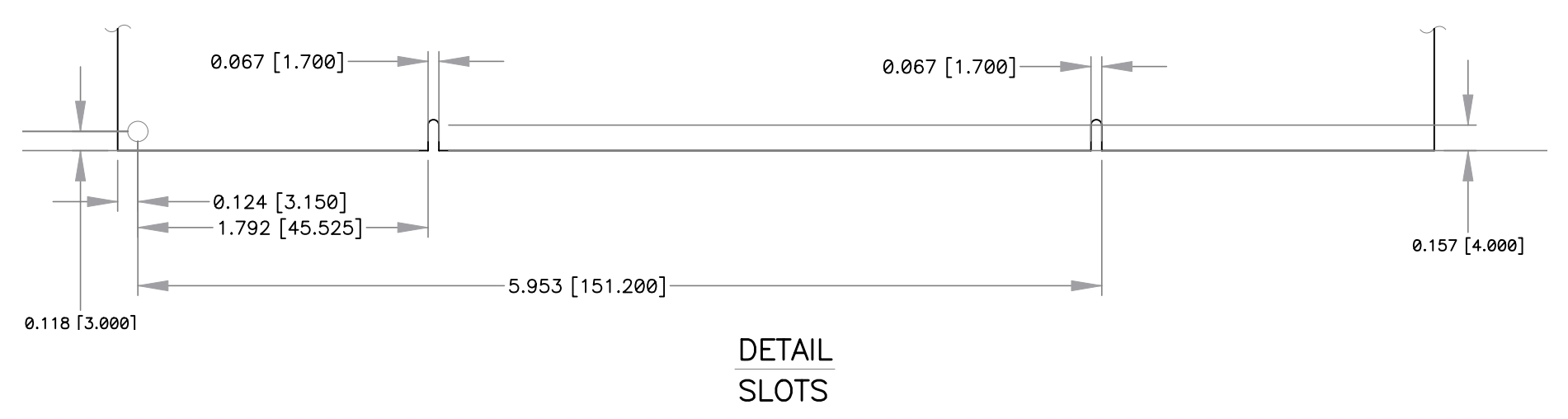
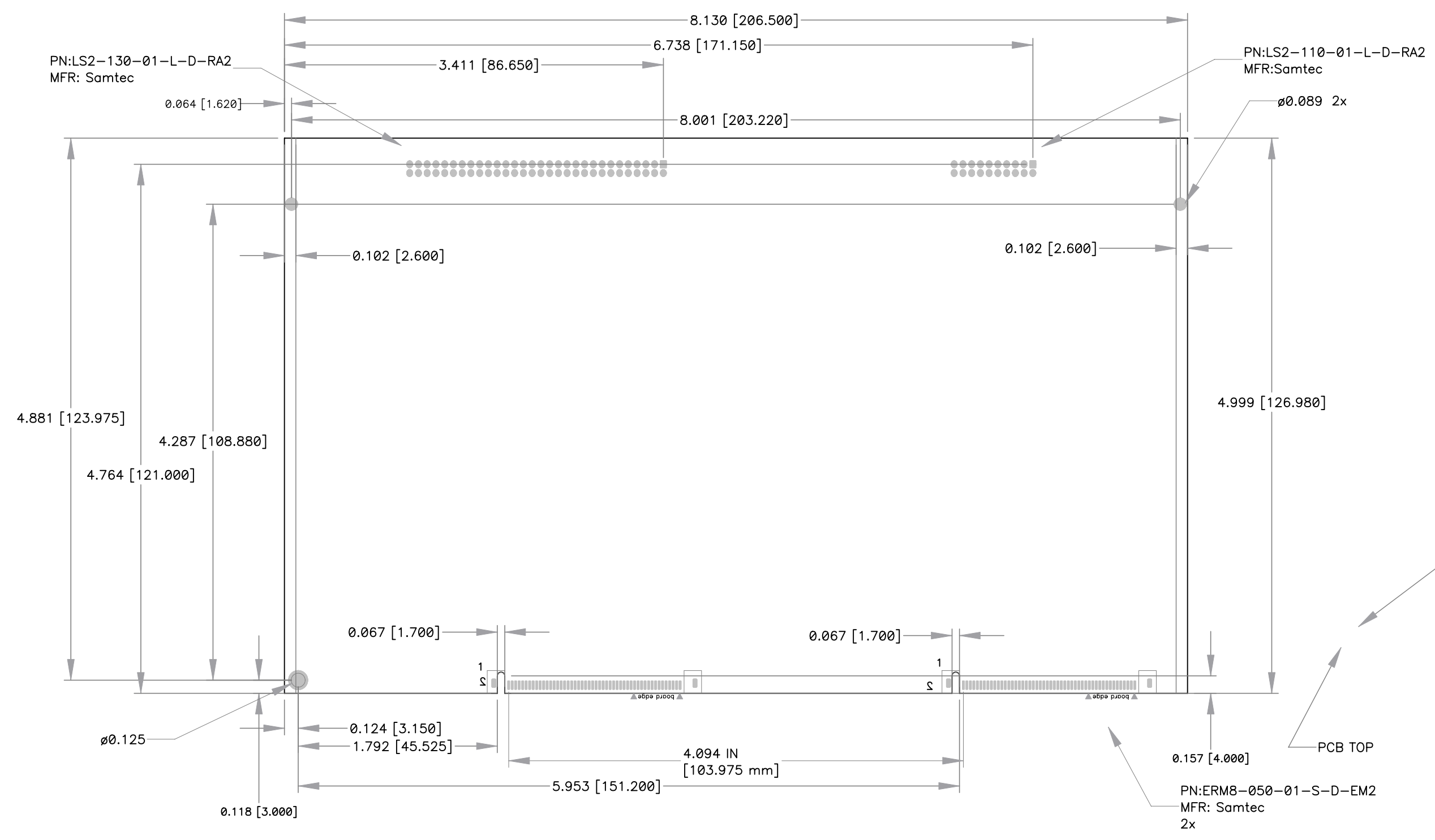


| ZONE | LTR | REVISIONS | ECR | DATE | BY | APRV |
|------|-----|-----------|-----|------|----|------|
| | | | | | | |



- NOTES:
- Designations of the edge mount connectors shall be P1 & P2.
 - Designations of the right angle through hole connectors LS2-130... is J1, LS2-110 is J2, shall be mounted on the top side of the pcb.
 - All test points and identification markings shall be on the bottom side of the pcb.
 - For the purpose of contract automated assembly the board shall have 3 fiducials along it's outer extremities on both top and bottom of the board.
 - To maintain interboard separation the maximum component height for the top of the board is 0.500 inches and .180 inches for the bottom.
 - The .089 dia holes are for the retention pin on the card guide to pass through.
 - Maintain component keepout area on both sides of board along card edges.

| QTY REQ'D | PART OR IDENTIFYING NO | ITEM DESCRIPTION | ITEM NO |
|--------------------------------------|------------------------|--|--|
| TOLERANCES UNLESS OTHERWISE NOTED | | <p>THIRD ANGLE PROJECTION</p> | <p>NATIONAL OPTICAL ASTRONOMY OBSERVATORIES</p> <p>OPERATED BY THE</p> <p>ASSOCIATION OF UNIVERSITIES FOR RESEARCH IN ASTRONOMY</p> <p>UNDER COOPERATIVE AGREEMENT WITH</p> <p>NATIONAL SCIENCE FOUNDATION</p> |
| .XX ± .03 | ANGULAR | | |
| .XXX ± .010 | ±.5° | <p>DO NOT SCALE DRAWING</p> <p>NEXT ASSEMBLY</p> <p>REFER TO SCHEMATIC</p> | |
| SCALE: FULL | | DESIGNED BY: Dee Stover | DATE: 05oct09 |
| DWG PRODUCED USING: PCAD 2006 | | DRAWN BY: Dee Stover | APPROVED BY: [Signature] |
| DWG NO: TRNT-AD-01-0001 | | DATE: 05oct09 | RELEASED: [Signature] |
| NAME: ICD | | USED ON: TORRENT | REF: NA |
| Name: Analog Front End Circuit Board | | DWG SIZE: C | REV: OD |
| SHEET 1 OF 1 | | SHEET 1 OF 1 | |