

REVISIONS

ZONE LTR REVISIONS ECR DATE BY APRV

A repositioned inductors in circuit, change to shield grounds 47uF cap pattern change MNSN-0110 05oct05 dms p.moore

NOTES:

- 1. Reference MNSN-EL-04-4010 Bill of Materials
- 2. Before installing any components check all boards using an ohm meter for shorts between all power and ground nodes.
- 3. All polarized caps are marked with a (+) sign on the positive node.
- 4. All diodes have a heavy line on the cathode side.
- 5. Parts can be crossed as long as the replacement is the same or an upgrade of the specified part.
- 6. Resistance values are in ohms, capacitance values are in uF (microfarads).
- 7. Make sure all axial leaded components are inserted with the value up and readable.
- 8. Holes and/or lands of components that are not installed should be kept free of solder.
- 9. This assembly contains electrostatic discharge (ESD) sensitive devices. Static free handling is required.
- 10. rJ3, rJ4, rJ5 are press fit connectors and SHALL be soldered in place to ensure connectivity.

VENDOR NOTE: NOTIFY NOAO OF ANY CONFLICTING REQUIREMENTS OR IF BOARDS CANNOT BE MANUFACTURED TO MEET THE REQUIREMENTS, DUE TO VENDORS PROCESS AND/OR TECHNIQUES OR BECAUSE PHOTO TOOLS AND/OR SPECIFICATIONS ARE INADEQUATE.

QTY REQ'D	PART OR IDENTIFYING NO			ITEM DESCRIPTION							ITEM NO
TOLERANCES UNLESS OTHERWISE NOTED .XX \pm .03 ANGULAR .XXX \pm .010 \pm .5 $^{\circ}$ THII			RD ANGLE PRO		NATIONAL OPTICAL ASTRONOMY OPERATED BY THE ASSOCIATION OF UNIVERSITIES FOR RESEARCH UNDER COOPERATIVE AGREEMENT NATIONAL SCIENCE FOUNDATION					IES	
DO	NOT SCALE	DRAWING	;	NAME	•			USED ON	RE	EF	
NEXT ASSEMBLY				ASSEMBLY				Monsoc	on		
REFER TO SCHEMATIC				Generic				DWG SIZE		REV	
MNSN-EL-04-2010				Clock & Bias Transition				С		Α	
SCALE:	FULL	DESIGNED BY D. Stover	DATE 0	11nov04	CHECKED BY	DATE	DWG NO M	VSN-E	L-0	4-00	10
DWG PRODU	JCED USING PCAD2004	DRAWN BY Dee Stover	DATE 1	5nov04	APPROVED BY	DATE	RELEASED	s	HEET '	1 OF 1	1